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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/676,844	09/29/2000	Bryan R. White	10559-165001/P8249	3643
20985	7590	03/20/2003		
FISH & RICHARDSON, PC 4350 LA JOLLA VILLAGE DRIVE SUITE 500 SAN DIEGO, CA 92122			EXAMINER MONESTIME, MACKLY	
			ART UNIT 2676	PAPER NUMBER
			DATE MAILED: 03/20/2003	

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No.	Applicant(s)
	09/676,844	WHITE, BRYAN R
Examiner	Art Unit	
Mackly Monestime	2676	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

1) Responsive to communication(s) filed on 29 September 2000.

2a) This action is FINAL.                            2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

4) Claim(s) 1-16 is/are pending in the application.

4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5) Claim(s) \_\_\_\_\_ is/are allowed.

6) Claim(s) 1-16 is/are rejected.

7) Claim(s) \_\_\_\_\_ is/are objected to.

8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11) The proposed drawing correction filed on \_\_\_\_\_ is: a) approved b) disapproved by the Examiner.

If approved, corrected drawings are required in reply to this Office action.

12) The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some \* c) None of:

1. Certified copies of the priority documents have been received.

2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.

3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).

a) The translation of the foreign language provisional application has been received.

15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

1) Notice of References Cited (PTO-892)                            4) Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_.

2) Notice of Draftsperson's Patent Drawing Review (PTO-948)                    5) Notice of Informal Patent Application (PTO-152)

3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 6.                    6) Other: \_\_\_\_\_

Art Unit: 2676

## **DETAILED ACTION**

1. Claims 1-16 are presented for examination.

### ***Claim Rejections - 35 USC § 102***

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless :

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371© of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) do not apply to the examination of this application as the application being examined was not (1) filed on or after November 29, 2000, or (2) voluntarily published under 35 U.S.C. 122(b). Therefore, this application is examined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

3. Claims 1-3, 7-9 and 13 are rejected under 35 U.S.C. 102(e) as being anticipated by Fisher et al (US Patent No. 6,480,200).

4. As per claims 1 and 13, Fisher et al disclosed the invention as claimed, including a memory controller hub comprising: a graphics subsystem adapted to perform graphics operations on data (Fig. 1, Item No. 18); and a cache adapted to store addresses of locations in physical

Art Unit: 2676

memory available to the graphics subsystem for storing graphics data (col. 1, lines 58-61) and available to the memory controller hub to store graphics data (Fig. 1, Item No. 18).

5. As per claim 7, Fisher et al disclosed the invention as claimed, including a CPU (Fig. 1, Item No. 10); a display device (Fig. 1, Item No. 24); a system memory adapted to store video data and non-video data; and a memory controller hub coupled to the CPU (Fig. 1, Item No. 12) and coupled to the system memory (Fig. 1, Item No. 14), the memory controller hub comprising: a graphics subsystem adapted to perform graphics operations on data (Fig. 1, Item No. 18); and a cache adapted to store addresses of locations in physical memory available to the graphics subsystem for storing graphics data (col. 1, lines 58-61) and available to the memory controller hub to store graphics data (Fig. 1, Item No. 18).

6. As per claims 2-3 and 8-9, Fisher et al disclosed a dedicated bus interface coupling to the graphics controller to the memory controller hub (Fig. 1, Item No. 22)

***Claim Rejections - 35 USC § 103***

7. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Art Unit: 2676

8. Claims 4-6, 10-12 and 14-16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Fisher et al (US Patent No. 6,480,200) in view of Surti et al (US Patent No. 6,496,193).

9. As per claims 4-6, 10-12 and 14-16, Fisher et al further disclose a cache adapted to store addresses of locations in physical memory (col. 1, lines 58-61), but Fisher et al did not explicitly disclose that the memory controller hub is configured to provide a block of linear, virtual memory address by graphics subsystem or graphics controller. However, Surti et al disclosed a method and apparatus for fast loading of texture data into a tile memory, wherein texture data are stored in system memory in a tile format that allows an entire cache tile to be stored linearly in memory space (col. 2, lines 8-10; col. 4, lines 3-5, 47-51). It would have been obvious to a person of ordinary skill in the art at the time the invention was made to combine the cited references because doing so would not only allow the graphics engine to access memory faster without causing an excessive number of page misses in the memory subsystem; but also allow the graphics engine to take advantage of the high burst rate fills to load the cache, thereby enhance the processing speed of the system.

### *Conclusion*

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mackly Monestime whose telephone number is (703) 305-3855. The examiner can normally be reached on Monday to Thursday from 7:30 AM to 6:00 PM.

Art Unit: 2676

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bella Matthew, can be reached on (703) 308-6829.

**Any response to this action should be mailed to:**

Commissioner of Patent and Trademarks

Washington, D.C. 20231

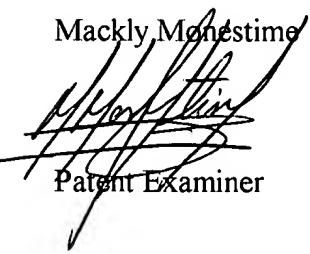
**or faxed to:**

**(703) 872-9314 (for Technology Center 2600 only)**

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, Va, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.

Mackly Monestime

  
Patent Examiner

March 12, 2003



MATTHEW C. BELLA  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2600